

## Silicon CAD PD Back End

### Description

Join a cutting-edge and well-funded hardware startup as a Physical Design CAD Engineer. Our mission is to reimagine silicon and create computing platforms that will transform the industry. You will have the opportunity to work with some of the most talented and passionate engineers in the world to create designs that push the envelope on performance, energy efficiency and scalability. We offer a fun, creative and flexible work environment, with a shared vision to build products to change the world.

**Responsibilities**

- P&R construction flows and analysis flows for PPA (performance, power, area) and reliability (signal/power integrity, etc.)
- Employ good software engineering practices to develop leading-edge EDA/CAD tools for chip development
- Work closely with custom and physical design engineers to train and support them and identify possible improvements and solutions
- You will be directly responsible for the full life-cycle of tool development including flow development, testing, deployment, and maintenance
- Interface with EDA vendors to solve issues, evaluate tools, and improve tools
- Requirements
- Strong scripting skills in multiple languages (Shell and Tcl are a must, Perl and Python are good to know)
- Familiar with flow development including creating a good user interface, testing/improving run-time, debugging issues
- Knowledge of EDA tools such as Genus, Innovus, Quantus, Tempus, Joules, Virtuoso, Nanotime, Spice and preferably how they work under the hood
- Strong technical background in VLSI, ASIC, and EDA fundamentals
- Background in physical or custom design is a plus
- Self-starter with passion for growth, ability to quickly learn, enthusiasm for continuous learning and sharing findings across the team
- Excellent skills in problem solving, written and verbal communication, excellent organization skills, and highly self-motivated
- Ability to work well in a team, multi-task, and be productive under aggressive schedules

**Education and Experience**

- PhD, Master's Degree or Bachelor's Degree in technical subject area
- Please mention the word **\*\*FAVORITED\*\*** and tag **RMy44MC4yMTUuMTM2** when applying to show you read the job post completely (#RMy44MC4yMTUuMTM2). This is a beta feature to avoid spam applicants.
- Companies can search these words to find applicants that read this and see they're human.

### Contacts

Job listing via [RemoteOK.com](https://www.RemoteOK.com)

### Hiring organization

Rivos

### Job Location

Santa Clara, CA

### Base Salary

\$ 77500 - \$ 115000

### Date posted

May 25, 2024

[Apply Now](#)